

Inventor(s): Oi et al.

Appl. No.: 09

421,322

Series Code ↑

Serial No. ↑

Filed: October 18, 1999

Hon. Commissioner of Patents

Washington, D.C. 20531

Sr. DEC 28 2001

REPLY/AMENDMENT/LETTER

Group A Unit

2815

Examiner:

G. Eckert, II

Atty. Dkt.

P 0264817

M#

Client Ref

Appl. Title: Dielectrically Separated Wafer and Method of Manufacturing the Same

Date: December 27, 2001j

This is a ~~Trademark~~ amendment/letter in the above-identified application and includes the herewith attachment of same date and subject which is incorporated hereinto by reference and the signature below is treated as the signature to the attachment in absence of a signature thereto.

FEE REQUIREMENTS FOR CLAIMS AS AMENDED

1. Small Entity claim A. <input checked="" type="checkbox"/> NOT made B. <input type="checkbox"/> Withdrawn C. <input type="checkbox"/> made herewith D. <input type="checkbox"/> made previously For B & C See Required Separate Paper (Pat-256)		Claims remaining after amendment	Highest number previously paid for	Present Extra	Large/Small Entity	Additional Fee	Fee Code Lg/Sm
2. Total Effective Claims		5	**minus 20	0	x \$18/\$9 =	+ \$0	103/203
3. Independent Claims		1	***minus 3	0	x \$84/\$42 =	+ \$0	102/202
4. If amendment enters proper multiple dependent claim(s) into this application for first time (leave blank if this is a reissue application)..... add						+ \$280/\$140 =	+ \$0 104/204
5. Original due Date: December 19, 2001		<input type="checkbox"/> NONE					
6. Petition is hereby made to extend the original due date to cover the date this response is filed for which the requisite fee is attached		(1 mo)	\$110/\$55 =	+ \$110			115/215
		(2 mos)	\$400/\$200 =				116/216
		(3 mos)	\$920/\$460 =				117/217
		(Usable only for ≤ 2mo.OA --- 4 mos)	\$1,440/\$720=				118/218
		(Usable only for 30 day/1mo.OA --- 5 mos)	\$1,960/\$980=				128/228
7. Enter any previous extension fee paid since above original due date and subtract				- \$0			
8. Extension Fee Attached				+ \$110			
9. If Terminal Disclaimer attached, add Rule 20(d) official fee				+ \$110/\$55		+ \$0 148/248	
10. If IDS attached requires Official Fee under Rule 97 (c),				+ \$180		+ \$0 126	
or if Rule 97(d) Request				+ \$180		+ \$0 126	
11. After-Final Request Fee per rules 129(a) and 17(r)				+ \$740/370		+ \$0 146/246	
12. No. of additional inventions for examination per Rule 129(b)				x \$740/370 ea		+ \$0 149/249	
13. Request for Continued Examination (RCE)				+ \$740/370		+ \$0 1179/1279	
14. Petition fee for						+ \$0	
15. TOTAL FEE ENCLOSED =						\$110	

16. *If the entry in this space is less than entry in next space, the "Present Extra" result is "0".

17. **If the "Highest number previously paid for" in this space is less than 20, write "20" in this space.

18. ***If the "Highest number previously paid for" in this space is less than 3, write "3" in this space.

Our Deposit Account No. 03-3975)

(Our Order No. 061063 0264817

C#

M#

CHARGE STATEMENT: The Commissioner is hereby authorized to charge any fee specifically authorized hereafter, or any missing or insufficient fee(s) filed, or asserted to be filed, or which should have been filed herewith or concerning any paper filed hereafter, and which may be required under Rules 16-18 (missing or insufficiencies only) now or hereafter relative to this application and the resulting Official Document under Rule 20, or credit any overpayment, to our Accounting/Order Nos. shown above, for which purpose a duplicate copy of this sheet is attached.

This CHARGE STATEMENT does not authorize charge of the issue fee until/unless an issue fee transmittal sheet is filed.

Query: Is appeal deadline now? If so, file Notice of Appeals separately.

Pillsbury Winthrop LLP

Intellectual Property Group

By Atty: G. Lloyd Knight

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NOTE: File this cover sheet in duplicate with PTO receipt (PAT-103A) and attachments

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION OF

OI *et al.*

Group Art Unit: 2815

Application No. 09/421,322

Examiner: ECKERT II, G

Filed: October 18, 1999

Title: DIELECTRICALLY SEPARATED WAFER AND METHOD
OF MANUFACTURING THE SAME



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TECHNOLOGY CENTER 2800
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1-15-02

December 27, 2001

T. Flanagan

* * * * *

AMENDMENT

Hon. Commissioner of Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated September 19, 2001 please amend the above identified application as follows.

IN THE CLAIMS

Please cancel claims 2, 4, 7 and 8 without prejudice or disclaimer.

Please enter the following amended claims:

1. (Amended) A dielectrically separated wafer having a plurality of dielectrically separated monocrystalline silicon islands mutually defined by a dielectrically separating oxide film on [the] a surface of the wafer, wherein said dielectrically separated silicon islands comprise:

a high concentration impurity layer formed on a bottom of the islands; and

a low concentration impurity layer having an identical conductivity laminated on the high concentration impurity layer.

3. (Amended) A dielectrically separated wafer having a polysilicon layer and a plurality of monocrystalline silicon islands mutually separated by a dielectrically separating